Effect of Nano Surface Topography on Electrical Properties of Lead Sulfide (PbS) Films Deposited on Polymer Substrate

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Abstract: Lead sulfide (PbS) films were deposited on polymide (PI) plastic substrate chemical spray pyrolysis technique at substrate temperature in the range 250-350 °C. Effects of substrate temperature on the electrical properties of the films were studied. In addition, the effect of nano surface topography on electrical properties of PbS films was studied. The deposited films were analyzed with atomic force microscope and Hall measurements to determine their electrical characteristics. AFM data shows that the root mean square (RMS) surface roughness of the PbS film increases with increasing substrate temperature. At room temperature, electrical resistivity of the PbS films varies from 9.54×10^3 to $1.14 \times 10^3 \Omega$.cm, respectively.

Keywords: PbS, Polyimide, Chemical Spray Pyrolysis, Substrate Temperature, Electrical properties.

1. INTRODUCTION

PbS is a IV-VI compound semiconductor has a cubic lattice with unit cell face center cube [1-3]. These semiconductor materials have important properties as direct band gap and narrow gap with band gap energy of 0.4 eV. PbS thin films have been used in a lot of applications, including electronic and optoelectronic devices [4-6].

Thin films of PbS have been prepared with various physical and chemical thin film deposition techniques, such as chemical bath deposition [7], physical vapor deposition [8] and spray deposition [9].

Research on PbS on flexible polymeric substrates such as polyimide (PI) is gaining immense interests due to their flexibility, light-weight, low-cost (therefore potentially low-cost devices can be fabricated on top of PI), high temperature resistance (typically up to 400 °C processing temperature), low coefficient of thermal expansion (CTE), low moisture uptake and high moisture release characteristics, excellent electrical properties and also increased voltage endurance [10]. Due to its superior properties, PI has found its applications as substrates in flexible thin film solar cells, flexible printed circuits and high density interconnects [11-13].

In this work, PbS thin films deposited on PI plastic substrate by chemical spray pyrolysis at different substrate temperatures (250, 300 and 350 $^{\circ}$ C). The influence of substrate temperature on the electrical

properties of the PbS thin films was observed. The results obtained has been compared and discussed with the specified results by several researchers.

2. EXPERIMENTAL PROCEDURE

2.1. Substrates Cleaning

In this experiment, PI plastic was used as substrate. The PI plastic substrate was cleaned by alcohol for 10 min to remove contamination. After the cleaning process, all of the substrates were rinsed with distilled water (DI water). The samples were then dried with nitrogen (N_2) gas.

2.2. Materials

Lead (II) acetate trihydrate (Pb(CH₃CO₂)₂ \cdot 3H₂O; 99.99%), Thiourea (CS (NH₂)₂), ≥99.0%)

2.3. Preparation of PbS Solutions

In the preparation of PbS films, aqueous solutions of Lead (II) acetate trihydrate (Pb(CH₃CO₂)₂·3H₂O and Thiourea (CS (NH₂)₂) with molarities of 0.1 M and appropriate volumes were mixed with distilled water by using magnetic stirrer for 30 minutes and the resultant solution was sprayed on PI plastic substrates by chemical spray pyrolysis technique at different temperature (250-350) °C. In order to get uniform thin films, the height of the spraying nozzle and the rate of spray process were kept constant during the deposition process at 35 cm and 15 cm³/min, respectively. The spraying process lasted about 6 second. The period between spraying processes was about 1 min; this period was enough to avoid excessive cooling of the substrate. The thickness of the prepared films was

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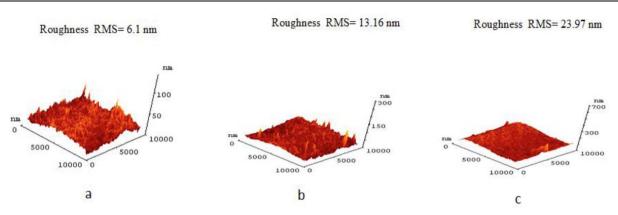


Figure 1: The AFM surface 3D images of PbS films as a function of substrate temperature: (a) 250 °C, (b) 300 °C and (c) 350 °C.

about 600 nm measured by gravimetric method. The electrical properties of PbS thin films was measured by Hall measurements (HL5500PC) system. The surface morphology of the PbS films were studied by atomic force microscope (AFM) (Model: ULTRA1Objective model).

3. RESULTS AND DISCUSSION

AFM images for surface morphologies of PbS deposited on PI plastic substrates are given in Figure **1a-c**. Figure **1** shows the variation of the film roughness with substrate temperature. The evaluated root mean square (RMS) surface roughness of the films, were 6.1nm , 13.16 nm and 23.97 nm for films with a substrate temperature of 250 °C , 300 °C and 350 °C nm, respectively.

It should be noted that the RMS surface roughness increases with increasing substrate temperature. This behavior approximates previous reports [14-16].

The electrical measurements were carried out by Hall measurements. The electrical study reveals that the films have degenerate and exhibit n-type electrical conductivity. Figure **2** shows the electrical behavior of the PbS films as a function of substrate temperature. The film resistivity decreases dramatically as the substrate temperature is increased from 250 to 350 °C. This behavior is in agreement with that reported in the literature [17, 18]. The evaluated of film resistivity was 9.54×10^3 , 3.2×10^3 and 1.14×10^3 (Ω .cm) at different temperature (250, 300 and 350 °C).

4. CONCLUSIONS

Lead sulfide (PbS) thin films were deposited on PI plastic substrates with chemical spray pyrolysis. Effects of substrate temperature on the electrical characteristics of the films deposited were experimentally investigated. The hall measurements show that the conductivity of the films is of p-type. The root mean square (RMS) roughness increased with increasing substrate temperature. It was observed that the increase of the substrate temperature decrease the resistivity film. This indicates with increasing the roughness, decrease the electrical resistivity of the films.

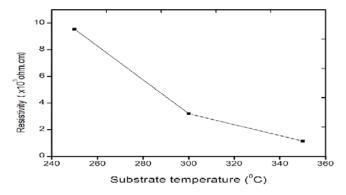


Figure 2: Resistivity of PbS films as a function of substrate temperature.

ACKNOWLEDGEMENTS

Hereby I would like to thank my home university "Koya University" and my sponsor the Ministry of Higher Education (MHE) of Kurdistan Regional Government (KRG) for enabling this study. The author thanks Mr. Mohd Zamir Pakhuruddin for help.

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DOI: http://dx.doi.org/10.6000/1929-6002.2016.05.01.3

Received on 02-11-2015

Accepted on 08-02-2016

Published on 02-03-2016

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